

**PATENT NUMBER and  
ISSUE DATE**

**U.S. UTILITY Patent Application**

<b>APPL NUM</b> 10050394	<b>FILING DATE</b> 01/16/2002	<b>CLASS</b> 257	<b>SUBCLASS</b>	<b>GAU</b> 2811	<b>EXAMINER</b> CKCU
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**\*\*CONTINUING DATA VERIFIED:** **BEST AVAILABLE COPY**

*None - TO*

**\*\* FOREIGN APPLICATIONS VERIFIED:**

*None - TO*

PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO F0466
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	
Verified and Acknowledged Examiner's status <i>TO</i>	
TITLE : Diode fabrication for ESD/EOS protection	

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>		
<b>ISSUE FEE</b> Amount Due: _____ Date Paid: _____		Assistant Examiner		Total Claims _____ Print Claim for O.G. _____
		Primary Examiner		<b>DRAWING</b> Sheets Drwg. _____ Figs. Drwg. _____ Print Fig. _____
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>				Application Examiner
		PREPARED FOR ISSUE		<p><b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent &amp; Trademark Office is restricted to authorized employees and contractors only.</p>

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